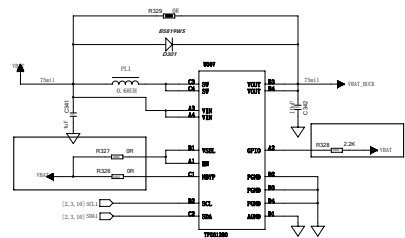
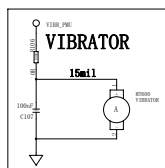
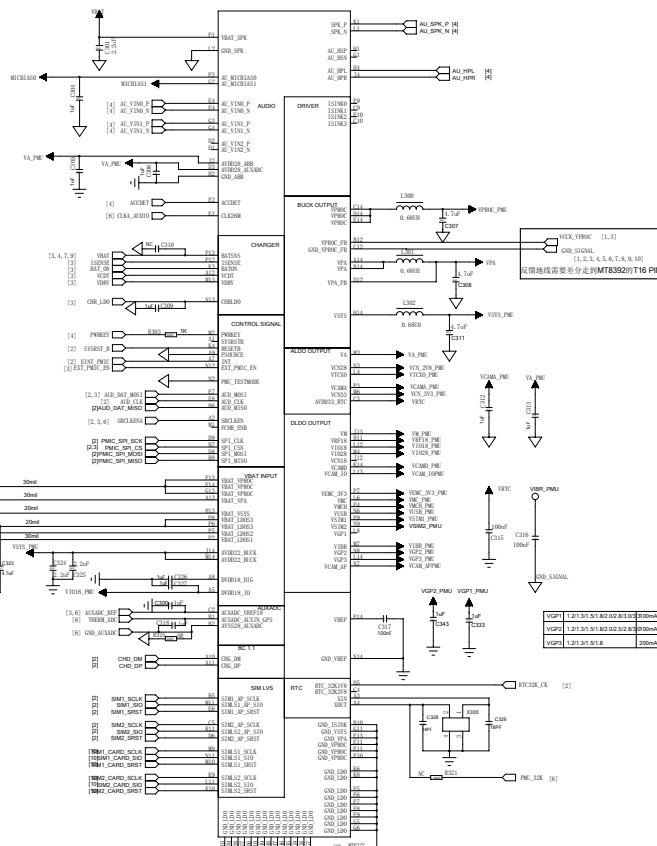
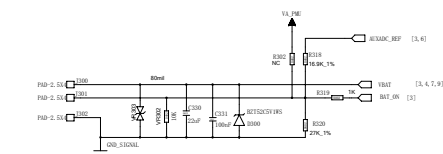
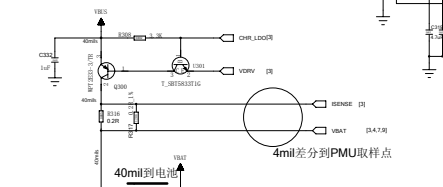
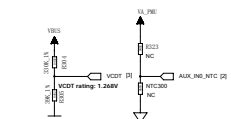
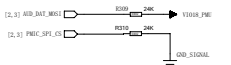


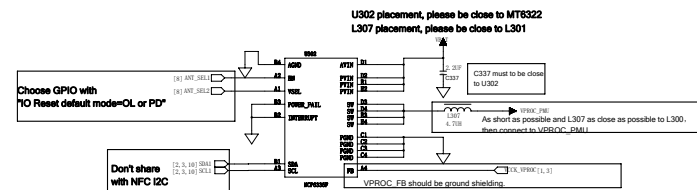
Companion buck for VBAT @ LV
TPS61280 I2C address: 0X75 (Write:0xEA, Read:0xEB)



Symbol	LPDDR2/1.2V	PCDDR3/1.35V	PCDDR3/1.5V	LPDDR1/1.8V	Default
SPL_CS#	H	L (20K)	H	L (20K)	PU
AUD_MOSI	L	H (20K)	H (20K)	L	PD

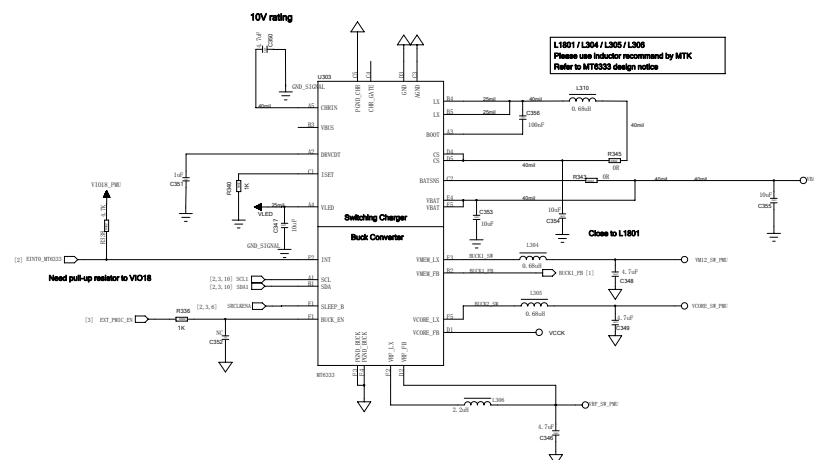


FAN53555 : 0xC0
NCP6335D: 0x1C



switching charger

MT6333 I2C Address: 0xD6, 0xD7



SPK

[R] AU_SPK_P 0R R405

[R] AU_SPK_N 0R R404

C402 100µF

C405 33µF

C406 33µF

W610

W611

J404 SPEAKER

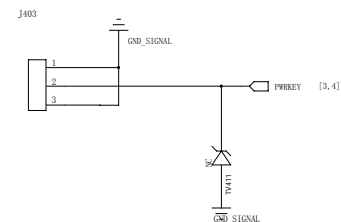
The schematic diagram illustrates the USB-to-serial converter circuit. It is divided into two main sections: the USB interface and the serial interface.

USB Interface:

- A 5V regulator is shown at the top left, consisting of an inductor L401 and a capacitor C415 (4.7µF).
- The USB-to-serial converter IC, U400 (SGM6003), is connected to the 5V regulator. Its SW pin is connected to the 5V line, and its VCC pin is connected to the 5V line. The FB pin is connected to ground.
- The USB connector D400 is connected to the SW pin of U400. The D+ pin of D400 is connected to the SW pin of U400. The D- pin of D400 is connected to the SW pin of U400. The GND pin of D400 is connected to ground.
- The SW pin of U400 is also connected to the SW pin of the 5V regulator.
- The SW pin of U400 is connected to the SW pin of the 5V regulator.
- The SW pin of U400 is connected to the SW pin of the 5V regulator.

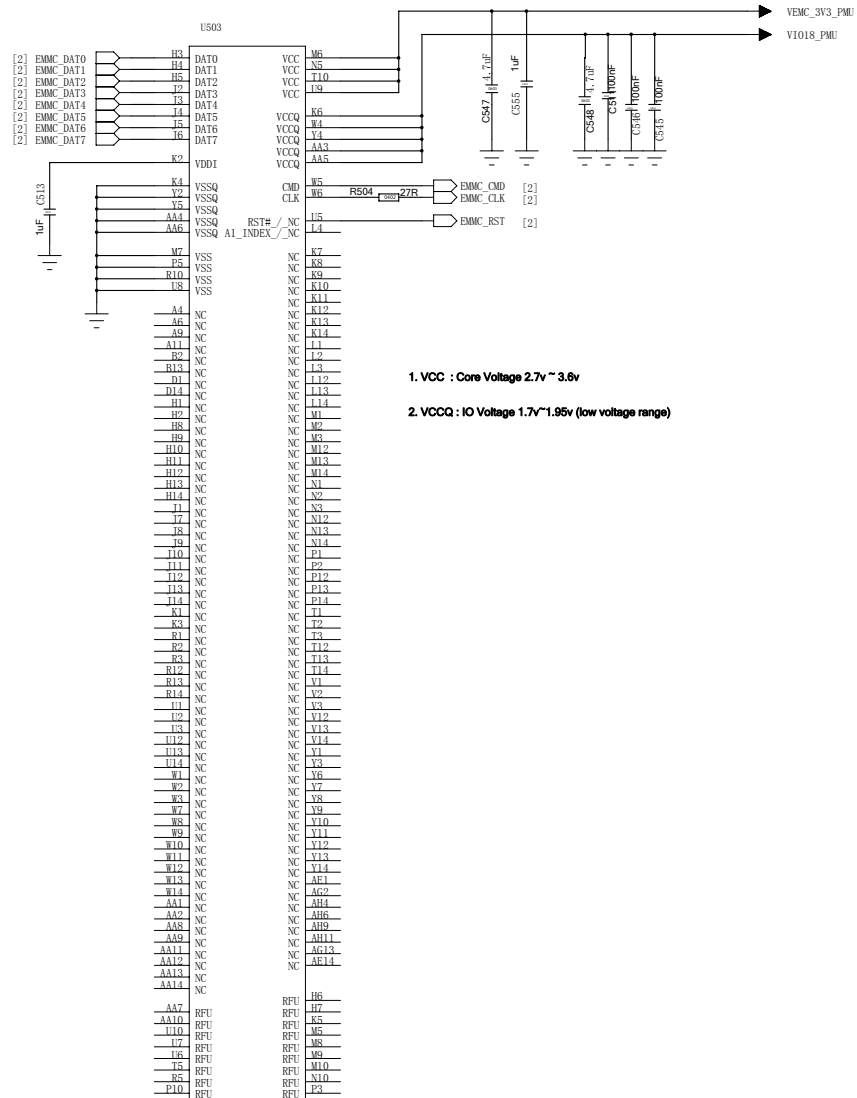
Serial Interface:

- The serial interface is shown at the bottom, featuring a J402 connector.
- The J402 connector is connected to a 1N4148 diode (R414) and a 5V regulator (L401, C415).
- The J402 connector has four pins: NC, NC, NC, and NC.
- The J402 connector is connected to the 1N4148 diode (R414) and a 5V regulator (L401, C415).
- The J402 connector is connected to the 1N4148 diode (R414) and a 5V regulator (L401, C415).
- The J402 connector is connected to the 1N4148 diode (R414) and a 5V regulator (L401, C415).

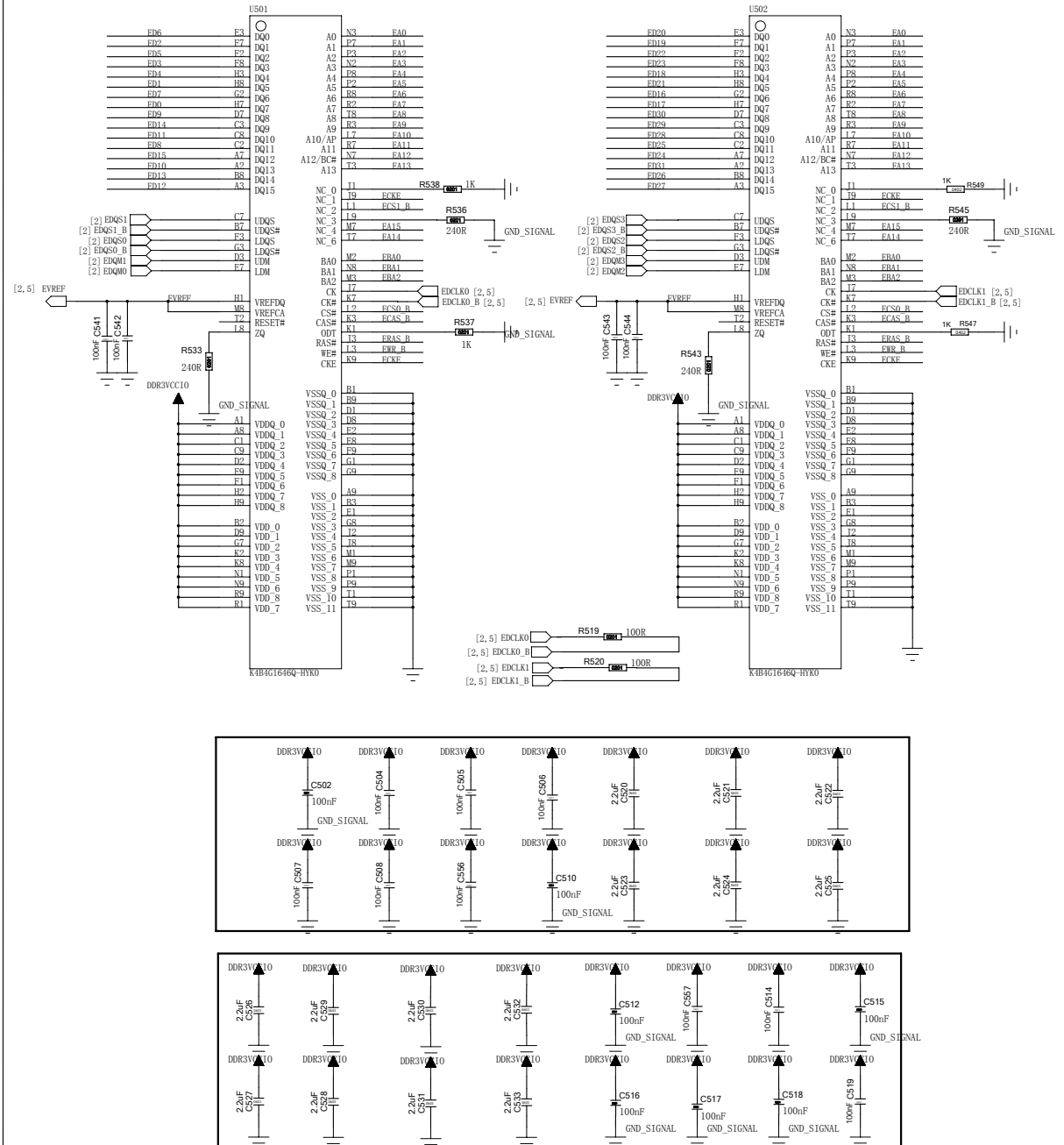
[illegible]

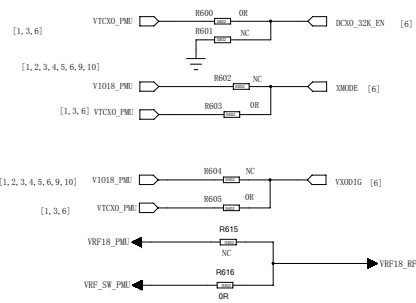
eMMC

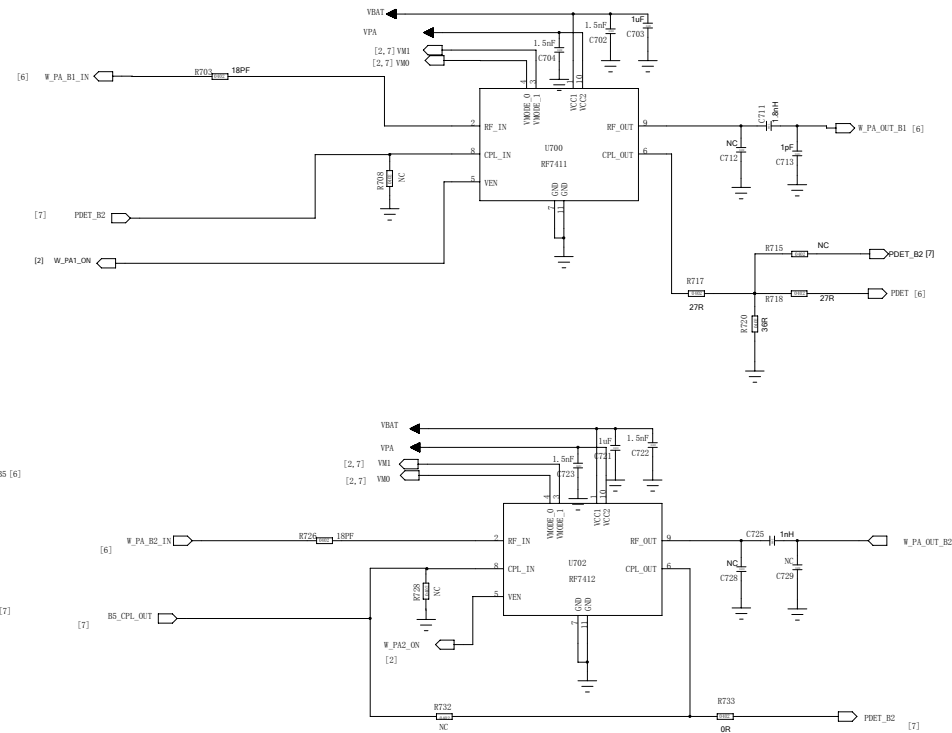
1. VCC : Core Voltage 2.7v ~ 3.6v
2. VCCQ : IO Voltage 1.7v~1.95v (low voltage range)

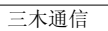
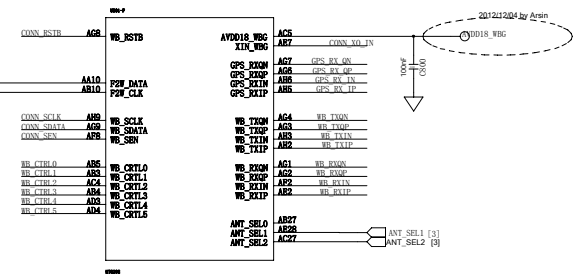


KLMAG2WEMB-B031









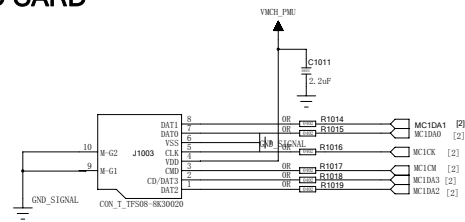
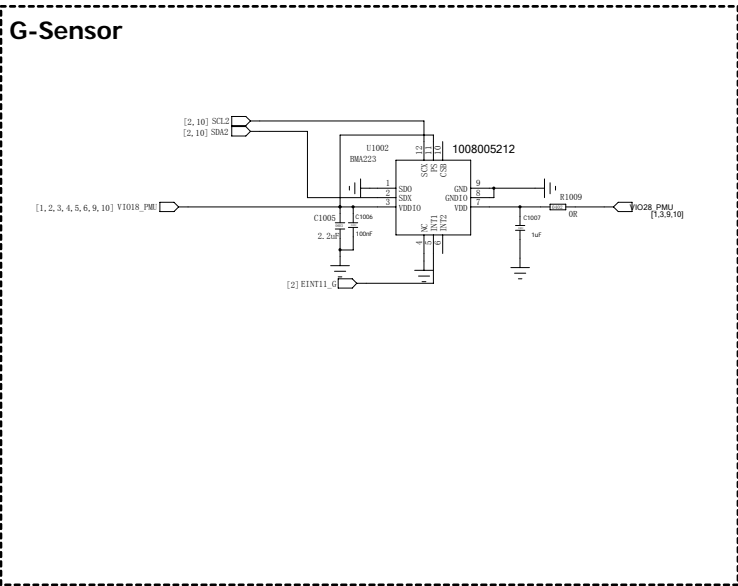
[illegible][illegible]

Figure 1 consists of two circuit diagrams, (a) and (b), showing the connection of SIM cards to the system. Both diagrams include a 100kΩ pull-up resistor connected to the I/O pin of the SIM card connector. The GND signal is connected to the GND pin of the connector.

(a) SIM1 connection to J1004. The SIM1 card connector pins are connected to the J1004 connector pins as follows:

- CLK (3.10) to CLK (1)
- SIM1_CARD_SCLK (3.10) to SCLK (3)
- SIM1_CARD_SRST (3.10) to SRST (5)
- VCC (3.10) to VCC (6)
- I/O (3.10) to I/O (2)
- VPP (3.10) to VPP (4)
- GND (3.10) to GND (8)

(b) SIM2 connection to J1005. The SIM2 card connector pins are connected to the J1005 connector pins as follows:

- CLK (3.10) to CLK (1)
- SIM2_CARD_SCLK (3.10) to SCLK (3)
- SIM2_CARD_SRST (3.10) to SRST (5)
- VCC (3.10) to VCC (6)
- I/O (3.10) to I/O (2)
- VPP (3.10) to VPP (4)
- GND (3.10) to GND (8)

